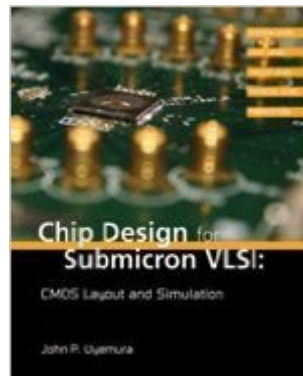


The book was found

# Chip Design For Submicron VLSI: CMOS Layout And Simulation



## Synopsis

The text is organized around first introducing the global view of digital integrated circuit design, VLSI and design automation, and then sequentially developing the topics from the materials and devices level, up through the circuits and then system level. This mirrors the structural hierarchy of the chip design field itself. While building a solid foundation and reference for the chip design, it integrates the discussion with hands-on examples of the design automation software, included in the book, to illustrate not only the layout and simulation concepts, but also how an industry designer would put them into practice. Both theory and application are effectively integrated into a cohesive treatment of the subject and art of chip design.

## Book Information

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## Customer Reviews

I would like to write about "Chip Design for Submicron VLSI:CMOS Layout and Simulation" book (2006 ed., THOMSON), written by John P. Uyemura. General : As the author mentioned that the book is a basic introduction to submicron CMOS designs, you will find the book contents organized into short chapters with a level of details that one can study and understand within a short period. The software (Microwind and Dsch) that comes with the book is a nice tool to start learning CMOS VLSI layout and simulation. It would be best to practice as you read and understand each section or topic. You can learn much from hands on by doing your own version of layouts or circuits for simulation. Each figure of a layout shown in the book is usually large enough to clearly see the details. Thus, you can try to recreate your own layout as seen from the figure. An important note

about using Dsch program should be given here. In a Dsch schematic, you cannot name an input with "/" as a part of the name, if you plan to compile the circuit to Verilog code, otherwise you will get a Compile Verilog file error. Author's Writing Style: The author is one of well known writers in the field of CMOS VLSI circuits and systems. If you have seen this book, and maybe also some other books written by him in a book store, you probably agree that his books are quite easy to read due to a clear and concise organization and a way he usually writes to convey information and present ideas. Key words are usually highlighted in each section. This helps for finding related explanation or specific concepts and ease of reviews. Errata: I found only a few (might be sent to publisher), the book is well written.

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